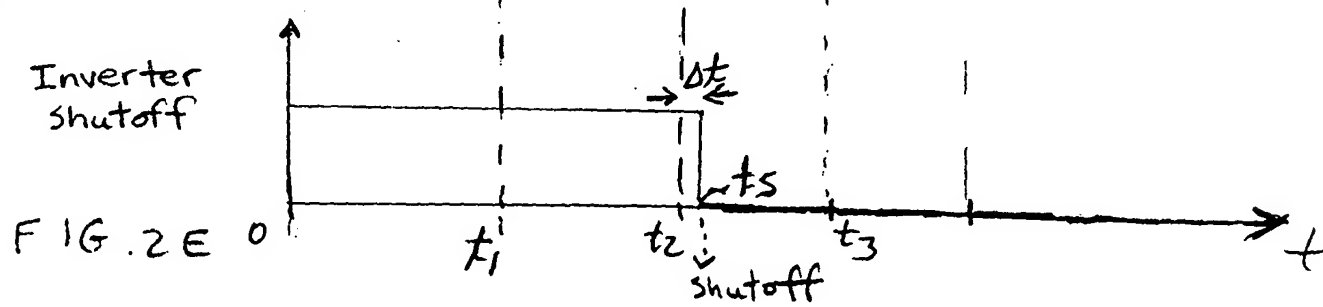
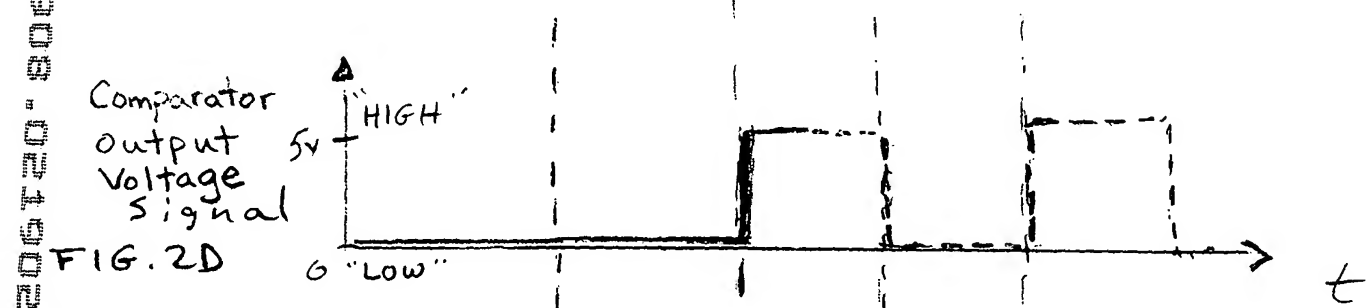
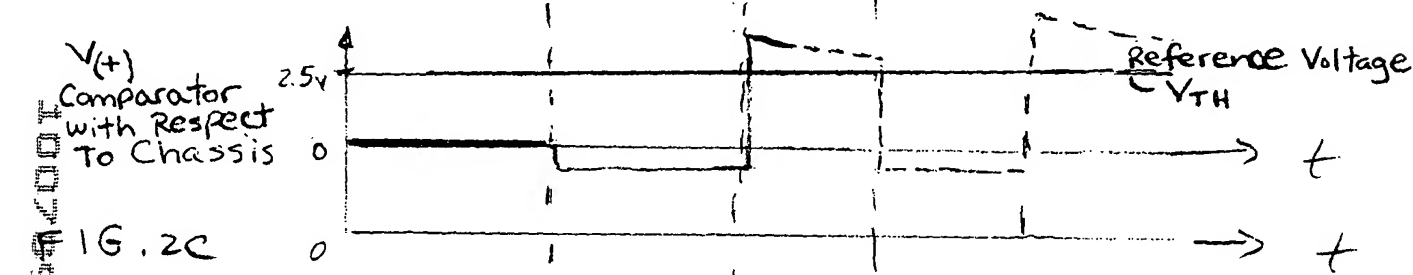
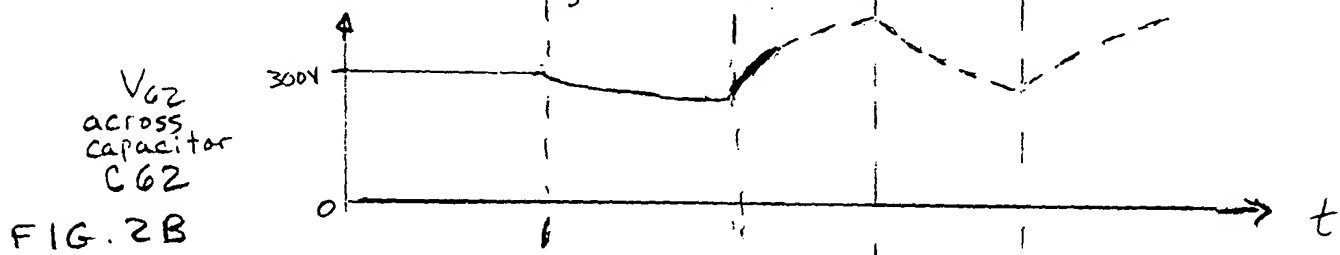
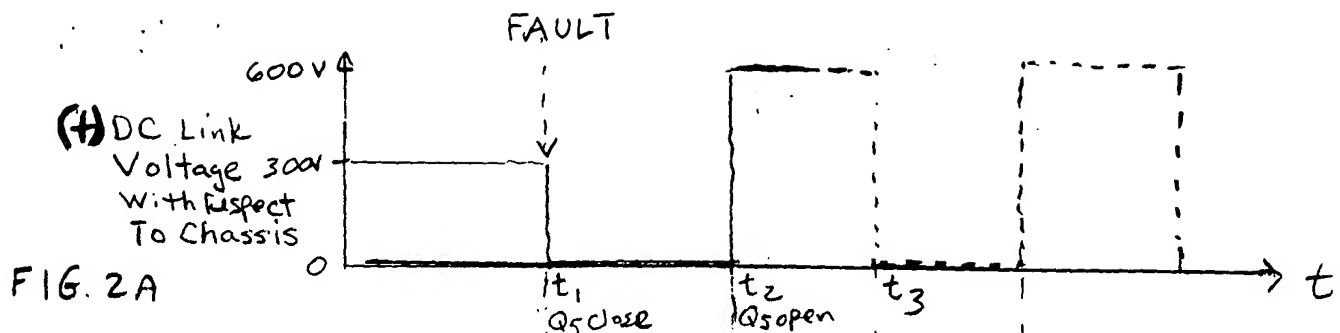
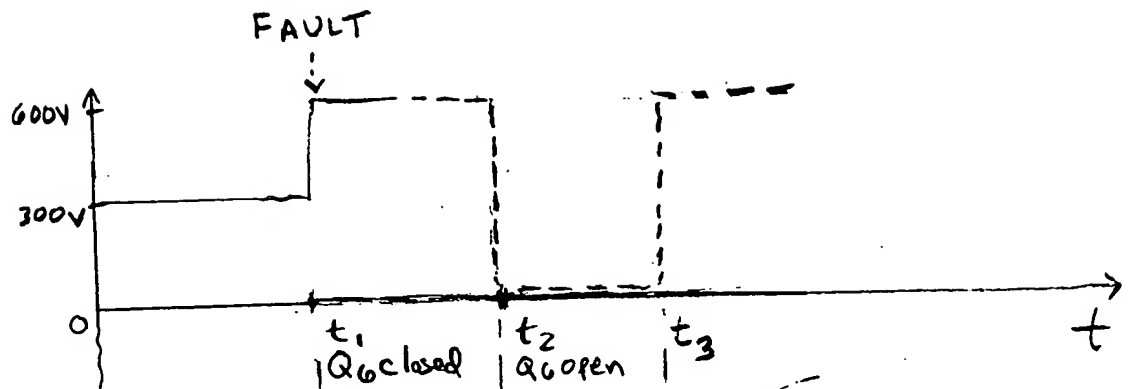


FIG. 1



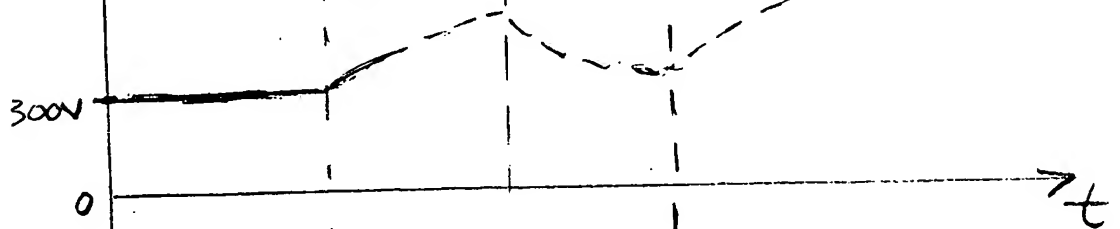
(+)DC Link Voltage
with Respect
to Chassis

FIG. 3A



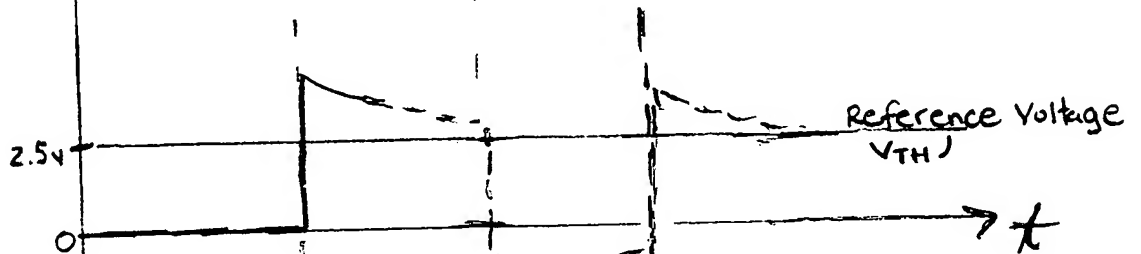
V_{C2}
across Capacitor
 C_{C2}

FIG. 3B



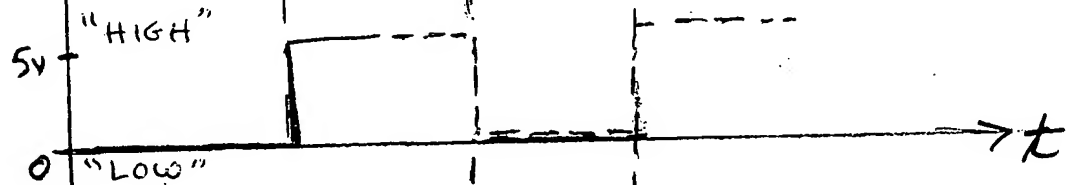
$V(t)$
Comparator
with Respect
to Chassis

FIG. 3C



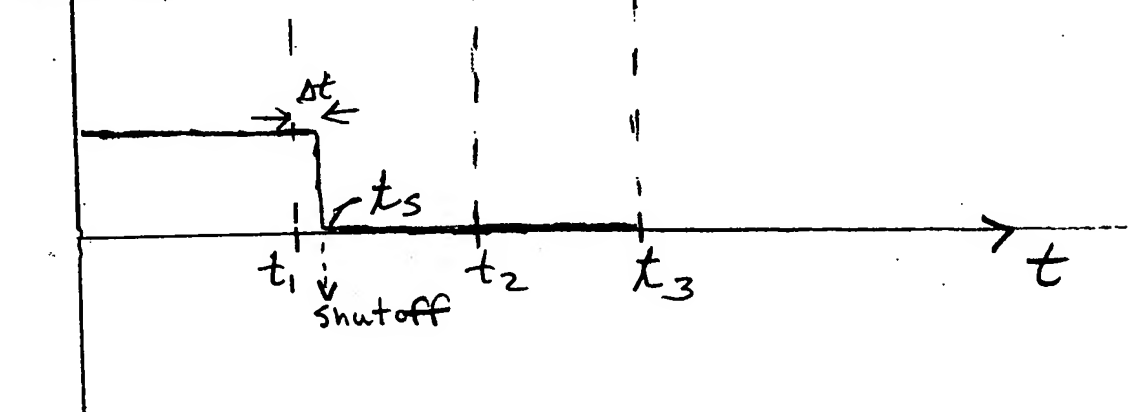
Comparator
Output Voltage

FIG. 3D

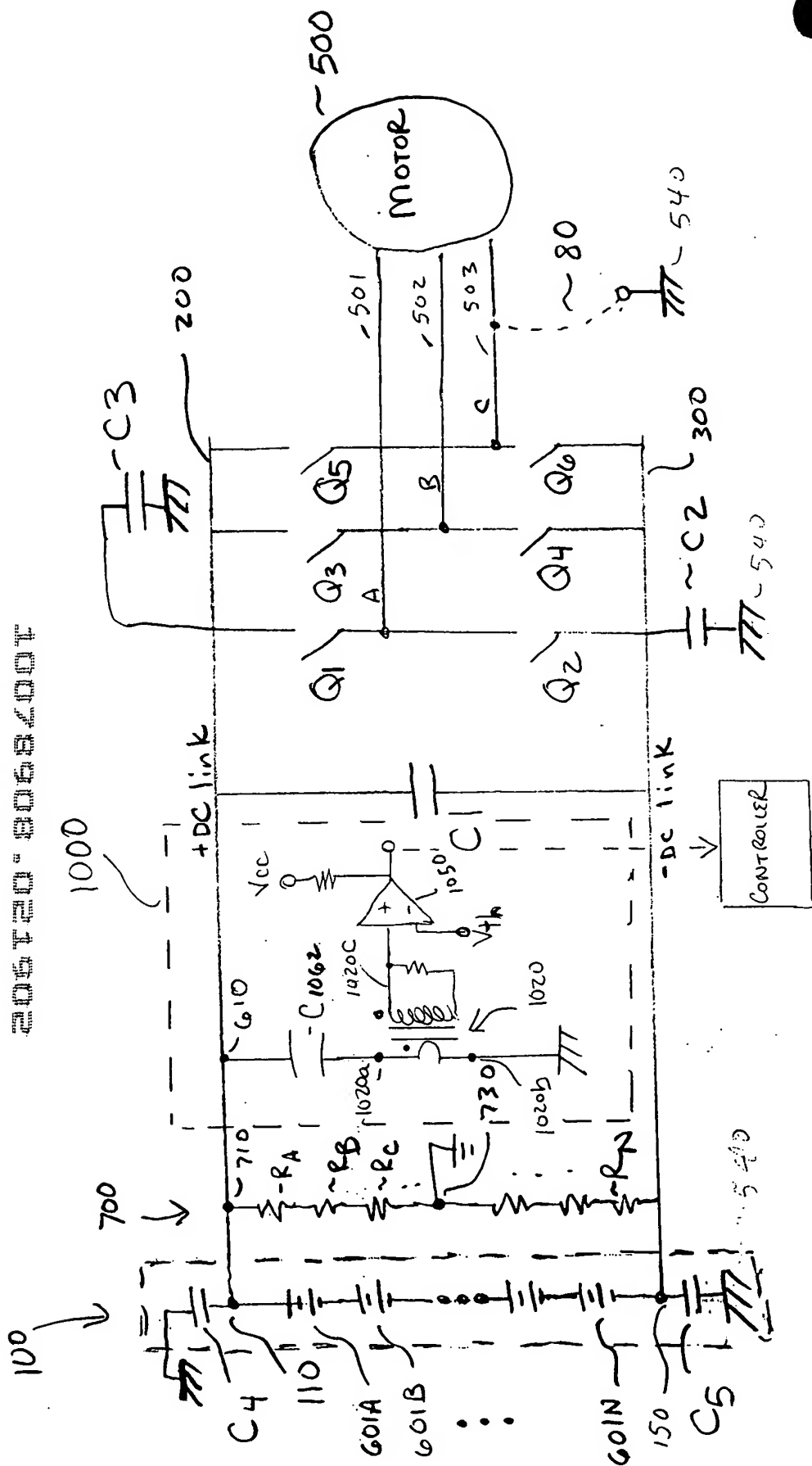


Inverter
Shutoff

FIG. 3E



100 →





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